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| APPLICATION NO | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|----------------|-------------|----------------------|---------------------|------------------|
| 10/670,532 | 09/26/2003 | Yutaka Ogawa | HITA.0435 | 6509 |

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-------------|------------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$1700 | 02/08/2005 |
| EXAMINER | | ART UNIT | CLASS - SUBCLASS |] ' | |
| LAM DAVID | | 2818 | 365-154000 | | |

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- 2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a
- single firm (have as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed
- 1. Reed Smith LLP
- 2. Stanley P. Fisher, Esq.
- 3. Juan Carlos A. Marquez, Esq.

ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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RENESAS TECHNOLOGY CORPORATION HITACHI ULSI SYSTEMS CO., LTD.

Tokyo, Japan Tokyo, Japan

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The COMMISSIONER OF PATENTS AND TRADEMARKS is requested to apply the Issue Fee and Publication Fee (if any) to the application identified above

(Date) February 7, 2005 (Authorized Signature)

Stanley P. Fisher Rog. No.: 24,344
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